Vector analysis and control of advanced static VAR compensators

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Abstract: The advanced static VAR compensator (ASVC) uses a high power self-commutating inverter to draw reactive current from a transmission line. Two fundamentally different types of inverter can be used for this purpose, one providing control of output voltage magnitude and phase angle, and the other having only phase angle control. For each of these types, the governing equations are derived, and frequency domain analysis is used to obtain the relevant transfer functions for control system synthesis. Further analysis is provided to determine the response of the STATCON to negative sequence and harmonic voltage components on the transmission line. The results are illustrated with measured waveforms obtained from a scaled analogue model of an 80 MVAR STATCON.

1 Introduction

The advanced static VAR compensator (ASVC) is based on the principle that a self-commutating static inverter can be connected between three-phase AC power lines and an energy-storage device, such as an inductor or capacitor, and controlled to draw mainly reactive current from the lines. This capability is analogous to that of the rotating synchronous condenser and it can be used in a similar way for the dynamic compensation of power transmission systems, providing voltage support, increased transient stability, and improved damping [1, 2]. The ASVC inverter requires gate-controlled power switching devices such as GTO thyristors. GTOs are now available with ratings that are sufficiently high to make transmission line applications feasible. Consequently the ASVC has become an important part of the flexible AC transmission system (FACTS), introduced by Hingorani [3], and presently being promoted by the Electric Power Research Institute (EPRI).

The EPRI has recently commissioned the design and construction of a scaled model of an 80 MVAR ASVC for transmission lines [4]. The model represents an optimum power circuit configuration based on a voltage-sourced inverter, and includes the control system that would be applied to a fullpower installation. The control system has been designed to achieve fast dynamic control of the instantaneous reactive current drawn from the line. This capability ensures that the ASVC will function usefully during transmission line disturbances. The concept of instantaneous reactive current is a new one and will be explained in the following Sections.

In the course of this project, the dynamic behaviour of the ASVC has been studied in depth. This paper presents a simplified mathematical model of the ASVC that has made it possible to derive the transfer functions needed for control system synthesis. The resulting control system designs are briefly outlined and further analysis presented to show the behaviour of the ASVC when the line voltage is unbalanced or distorted. The analysis is based on a vectorial transformation of variables, first described by Park [5] for AC machine analysis, and later, using complex numbers, by Lyon [6] in the theory of instantaneous symmetrical components.

2 Derivation of ASVC mathematical model

2.1 Instantaneous reactive current

The main function of the ASVC is to regulate the transmission line voltage at the point of connection. It achieves this objective by drawing a controlled reactive current from the line. In contrast with a conventional static VAR generator, the ASVC also has the intrinsic ability to exchange real power with the line. As there are no sizeable power sources or sinks associated with the inverter and its DC-side components, the real power must be actively controlled to a value which is zero on average and which departs from zero only to compensate for the losses in the system.

The notion of reactive power is well known in the phasor sense. However, to study and control the dynamics of the ASVC within a subcycle time frame and subject to line distortions, disturbances and unbalance, we need a broader definition of reactive power which is valid on an instantaneous basis.

The instantaneous real power at a point on the line is given by

\[ P = v_a i_a + v_b i_b + v_c i_c \]  (1)

The ASVC scaled model was designed and built at the Westinghouse Science and Technology Center through the combined efforts of several individuals. In particular, the authors would like to acknowledge the important contributions made by Mr. M. Gernhardt and Mr. M. Brennen.
We can define the instantaneous reactive current conceptually as that part of the three-phase current set that could be eliminated at any instant without altering $P$.

![Vector representation of instantaneous three-phase variables](image)

**Fig. 1** Vector representation of instantaneous three-phase variables

The algebraic definition of instantaneous reactive current is obtained by means of a vectorial interpretation of the instantaneous values of the circuit variables, as explained in the following Section.

### 2.2 Vector representation of instantaneous three-phase quantities

A set of three instantaneous phase variables that sum to zero can be uniquely represented by a single point in a plane, as illustrated in Fig. 1. By definition, the vector drawn from the origin to this point has a vertical projection onto each of three symmetrically disposed phase axes which corresponds to the instantaneous value of the associated phase variable. This transformation of phase variables to instantaneous vectors can be applied to voltages as well as to currents. As the values of the phase variables change, the associated vector moves around the plane describing various trajectories. The vector contains all the information on the three-phase set, including steady-state unbalance, harmonic waveform distortions, and transient components. Fig. 2 provides a graphical illustration of the vector trajectory that would develop in the case of a three-phase set with severe harmonic distortion. The diagram shows the vector trajectory and relates it back to the actual phase-variable waveforms.

![Example of vector trajectory: 25% fifth harmonic](image)

**Fig. 2** Example of vector trajectory: 25% fifth harmonic

In Fig. 3, the vector representation is extended by introducing an orthogonal co-ordinate system in which each vector is described by means of its $ds$- and $qs$-components. The transformation from phase variables to $ds$ and $qs$ co-ordinates is as follows:

$$[c]^{-1} = \frac{2}{3} [c] = \begin{bmatrix}
1 & -\frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\
0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \\
1 & 1 & 1
\end{bmatrix}
$$

$$\begin{bmatrix}
i_{ds} \\
i_{qs}
\end{bmatrix} = [c]^{-1} \begin{bmatrix}
v_a \\
v_b \\
v_c
\end{bmatrix} = \begin{bmatrix}
i_{ds} \\
i_{qs}
\end{bmatrix}
$$

**Fig. 3** Definition of orthogonal co-ordinates

Using eqns. 2, the instantaneous power given by eqn. 1 can be rewritten in terms of $ds$ and $qs$ quantities as follows:

$$P = \frac{3}{2} (v_a i_{ds} + v_b i_{qs}) = \frac{3}{2} |v||i| \cos (\phi)$$

where $\phi$ is the angle between the voltage and the current vectors. Clearly, only that component of the current vector which is in phase with the instantaneous voltage vector contributes to the instantaneous power. The remaining current component could be removed without changing the power, and this component is therefore the instantaneous reactive current. These observations can be extended to the following definition of instantaneous reactive power:

$$Q = \frac{3}{2} |v||i| \sin (\phi) = \frac{3}{2} (v_a i_{qs} - v_b i_{ds})$$

where the constant $3/2$ is chosen so that the definition coincides with the classical phasor definition under balanced steady-state conditions.

**Fig. 4** Shows how further manipulation of the vector co-ordinate frame leads to a useful separation of variables for power control purposes. A new co-ordinate system is defined where the $d$-axis is always coincident with the instantaneous voltage vector and the $q$-axis is in quadrature with it. The $d$-axis current component, $i_d$, accounts for the instantaneous power and the $q$-axis is...
current, \( i_r \), is the instantaneous reactive current. The \( d \) and \( q \) axes are not stationary in the plane. They follow the trajectory of the voltage vector, and the \( d \) and \( q \) co-ordinates within this synchronously rotating reference frame are given by the following time-varying transformation:

\[
[C_1]^{-1} = \frac{1}{3} [c_1] \quad \theta = \tan^{-1}\left(\frac{\dot{\psi}_d}{\dot{\psi}_q}\right)
\]

\[
\begin{bmatrix}
  i_d \\
  i_q \\
  v_d \\
  v_q
\end{bmatrix} =
\begin{bmatrix}
  \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 \\
  \frac{1}{\sqrt{2}} & -\frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 \\
  \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & -\frac{1}{\sqrt{2}} & 0 \\
  0 & 0 & 0 & 1
\end{bmatrix}
\begin{bmatrix}
  i_r \\
  i_{\theta} \\
  v_r \\
  v_{\theta}
\end{bmatrix} =
\begin{bmatrix}
  \frac{1}{\sqrt{2}} |v| & 0 \\
  0 & \frac{1}{\sqrt{2}} |v|
\end{bmatrix}
\]

and substituting in eqn. 1 we obtain

\[
P = \frac{1}{2} |v| i_d \quad Q = \frac{1}{2} |v| i_q
\]

Under balanced steady-state conditions, the co-ordinates of the voltage and current vectors in the synchronous reference frame are constant quantities. This feature is useful for analysis and for decoupled control of the two current components.

2.3 Equivalent circuit and equations

Fig. 5 shows a simplified representation of the ASVC, including a DC-side capacitor, an inverter, and series inductance in the three lines connecting to the transmission line. This inductance accounts for the leakage of the actual power transformers. The circuit also includes resistance in shunt with the capacitor to represent the switching losses in the inverter, and resistance in series with the AC lines to represent the inverter and transformer conduction losses. The inverter block in the circuit is treated as an ideal, lossless power transformer.

\[
L = \frac{\omega_b L}{z_{\text{base}}} \quad C = \frac{1}{\omega_b C_{z_{\text{base}}}} \quad R_p = R_{z_{\text{base}}} \quad R_d = R_{z_{\text{base}}}
\]

\[
\begin{bmatrix}
  i_d' \\
  i_q'
\end{bmatrix} =
\begin{bmatrix}
  -\frac{R_d}{L} & 0 & 0 \\
  0 & -\frac{R_q}{L} & 0 \\
  0 & 0 & -\frac{R_p}{L}
\end{bmatrix}
\begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} + \frac{\omega_b}{L} \begin{bmatrix}
  (e_d' - v_d) \\
  (e_q' - v_q)
\end{bmatrix}
\]

where \( p = \frac{d}{dt} \), and a per-unit system has been adopted according to the following definitions:

\[
L = \frac{\omega_b L}{z_{\text{base}}} \quad C = \frac{1}{\omega_b C_{z_{\text{base}}}} \quad R_p = R_{z_{\text{base}}} \quad R_d = R_{z_{\text{base}}}
\]

\[
\begin{bmatrix}
  i_d' \\
  i_q'
\end{bmatrix} =
\begin{bmatrix}
  -\frac{R_d}{L} & 0 & 0 \\
  0 & -\frac{R_q}{L} & 0 \\
  0 & 0 & -\frac{R_p}{L}
\end{bmatrix}
\begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} + \frac{\omega_b}{L} \begin{bmatrix}
  (e_d' - v_d) \\
  (e_q' - v_q)
\end{bmatrix}
\]

Using the transformation of variables defined in eqn. 5, eqns. 7 can be transformed to the synchronously rotating reference frame as follows:

\[
\begin{bmatrix}
  i_d' \\
  i_q'
\end{bmatrix} =
\begin{bmatrix}
  -\frac{R_d}{L} & 0 & 0 \\
  0 & -\frac{R_q}{L} & 0 \\
  0 & 0 & -\frac{R_p}{L}
\end{bmatrix}
\begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} + \frac{\omega_b}{L} \begin{bmatrix}
  (e_d' - v_d) \\
  (e_q' - v_q)
\end{bmatrix}
\]

where \( \omega = \frac{d\theta}{dt} \). Fig. 6 illustrates the AC-side circuit vectors in the synchronous frame. When \( i_d' \) is positive, the ASVC is drawing inductive VARS from the line, and for negative \( i_d' \) it is capacitive.

2.4 Types of voltage-sourced inverter

Neglecting the voltage harmonics produced by the inverter, we can write a pair of equations for \( e_d' \) and \( e_q' \).

\[
e_d' = kv_{d_{dc}} \cos(\alpha)
\]

\[
e_q' = kv_{q_{dc}} \sin(\alpha)
\]

where \( k \) is a factor for the inverter which relates the DC-side voltage to the amplitude (peak) of the phase-to-neutral voltage at the inverter AC-side terminals, and \( \alpha \) is the angle by which the inverter voltage vector leads the line voltage vector. It is important to distinguish between two basic types of voltage-sourced inverter that can be used in ASVC systems.

Inverter Type 1 allows the instantaneous values of both \( \alpha \) and \( k \) to be varied for control purposes. Provided
that \( v' \) is kept sufficiently high, \( e' \) and \( e'' \) can be independently controlled. This capability can be achieved by various pulse-width-modulation (PWM) techniques that invariably have a negative impact on the efficiency, harmonic content, or utilisation of the inverter. Type I inverters are presently considered uneconomical for transmission line applications and their control will only be briefly considered here.

**Inverter Type II** is of primary interest for transmission line ASVCs. In this case, \( k \) is a constant factor, and the only available control input is the angle, \( \alpha \), of the inverter voltage vector. This case will be discussed in greater detail.

### 2.5 Inverter Type I control system

Inspection of eqn. 9 leads directly to a rule that will provide decoupled control of \( I_d \) and \( I_q \). The inverter voltage vector is controlled as follows:

\[
\begin{align*}
e_d' &= \frac{L}{\omega_b} (x_1 - \omega I_q) + |\nu'|
\end{align*}
\]

\[
\begin{align*}
e_q' &= \frac{L}{\omega_b} (x_2 + \omega I_d)
\end{align*}
\]

Substitution of eqns. 12 and 13 into eqn. 9 yields

\[
\begin{bmatrix}
\dot{x}_1 \\
\dot{x}_2
\end{bmatrix} =
\begin{bmatrix}
-R_c I_c & 0 \\
0 & -R_c I_c
\end{bmatrix}
\begin{bmatrix}
x_1 \\
x_2
\end{bmatrix}
+ \begin{bmatrix}
1 \\
1
\end{bmatrix}
\begin{bmatrix}
i_d' \\
i_q'
\end{bmatrix}
\]

Eqn. 14 shows that \( i_d' \) and \( i_q' \) respond to \( x_1 \) and \( x_2 \) respectively, through a simple first-order transfer function, with no crosscoupling. The control rule of eqns. 12 and 13 is thus completed by defining the feedback loops and proportional-plus-integral compensation as follows:

\[
\begin{align*}
x_1 &= (k_a + k_b) (i_d^* - i_d) \\
x_2 &= (k_a + k_b) (i_q^* - i_q)
\end{align*}
\]

The control is thus actually performed using feedback variables in the synchronous reference frame. The reactive current reference, \( i_q^* \), is supplied from the ASVC outer-loop voltage control system, and the real power is regulated by varying \( i_d^* \) in response to error in the DC-link voltage via a proportional plus integral compensation. A block diagram of the control scheme is presented in Fig. 7.

### 2.6 Further model development for inverter type II control

For Type II inverter control it is necessary to include the inverter and DC-side circuit equation in the model. The instantaneous power at the AC- and DC-terminals of the inverter is equal, giving the following power balance equation:

\[
v_{dc}^2 I_d = \frac{3}{2} (e_d' I_d + e_q' I_q)
\]

and the DC-side circuit equation is

\[
p v_d = -\omega C \left( i_d' + \frac{e_d'}{R_p} \right)
\]

Combining eqns. 9, 10, 11, 17 and 18, we obtain the following state equations for the ASVC:

\[
\begin{bmatrix}
i_d' \\
i_q'
\end{bmatrix} = \begin{bmatrix}
\alpha_b & 0 \\
0 & \alpha_b
\end{bmatrix}
\begin{bmatrix}
\dot{x}_1 \\
\dot{x}_2
\end{bmatrix} + \begin{bmatrix}
\omega_b v' \\
0
\end{bmatrix}
\]

\[
[A] =
\begin{bmatrix}
-\frac{R_c I_c}{L} & \omega & 0
\\
-\omega & -\frac{R_c I_c}{L} & \frac{k_b}{L}
\end{bmatrix}
\]

\[
\begin{bmatrix}
-3 k C \omega_a \cos(\alpha) \\
2 k C \omega_a \sin(\alpha)
\end{bmatrix}
\]

Steady-state solutions for eqn. 19 using typical system parameters are plotted in Fig. 8 as a function of \( \alpha_0 \) (subscript 0 denotes steady-state values). Note that \( i_{0q} \) varies almost linearly with respect to \( \alpha_0 \), and the range of \( \alpha_0 \) for one per-unit swing in \( i_{0q} \) is very small. Neglecting losses (i.e. \( R' = 0, R'' = \infty \)), the steady-state solutions
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The undamped poles of the system are thus at

\[ s = 0 \quad \text{and} \quad s = \pm j \omega_b \sqrt{1 + \frac{3kC'}{2L'}} \]  

(23)

The transfer function, eqn. 22, also has a pair of complex zeroes on the imaginary axis. These move along the imaginary axis as a function of \( i'_{0} \), occurring at lower frequency than the poles only when

\[ i'_{0} < \frac{2i_{0}}{3kC'} = i'_{0x} \]  

(24)

A numerical computation of \( \Delta L'(s)/\Delta a(s) \) from eqn. 21, including the losses, has been done for two operating points to illustrate the movement of the complex zeroes. Fig. 10A presents the result for each case in a plot of log gain and phase against frequency.

**Case 1: Full capacitive load**

\[ i'_{0} = -1.01 \text{ p.u.} \quad \alpha_{0} = -0.011 \text{ rad (0.63°)} \]

\[ \frac{\Delta L'(s)}{\Delta a(s)} = \frac{2893(s + 8.7 + j1330)(s + 8.7 - j1330)}{(s + 23.8)(s + 15.4 + j1476)(s + 15.4 - j1476)} \]

**Case 2: Full inductive load**

\[ i'_{0} = 1.07 \text{ p.u.} \quad \alpha_{0} = 0.01 \text{ rad (0.57°)} \]

\[ \frac{\Delta L'(s)}{\Delta a(s)} = \frac{2111(s + 11.4 + j1557)(s + 11.4 - j1557)}{(s + 23.8)(s + 15.4 + j1476)(s + 15.4 - j1476)} \]

While Case 1 is amenable to feedback control, Case 2 clearly has little phase margin near the system resonant frequency. The latter situation is typical for the condition \( i'_{0} > i'_{0x} \) (0.44 p.u. in this example.) A controller has been designed to overcome this problem by using nonlinear state-variable feedback to improve the phase margin when \( i'_{0} > i'_{0x} \). The nonlinear feedback function, \( \Delta q_{v} \), has the following form

\[ \Delta q_{v} = \Delta q_{v} - g(i'_{0} - i'_{0x}) \Delta v_{dc} \]  

(25)

where \( g \) is a gain factor to be set by design. Fig. 10B shows the transfer function, \( \Delta Q(s)/\Delta a(s) \), for the same operating points as Fig. 10A, with \( g = 2.0 \). The improved phase margin in Case 2 is clearly seen. The control scheme block diagram is shown in Fig. 11, with the additional integral compensation required to obtain zero
steady-state error in \( f_g \). This scheme has been implemented in the ASVC scaled model with a closed-loop control bandwidth set to approximately 200 rad/s. This makes it possible to swing between full inductive mode and full capacitive mode in slightly more than a quarter of a cycle.

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3 Line voltage unbalance and harmonic distortion

With balanced sinusoidal line voltage and an inverter pulse number of 24 or greater, and ASVC draws no low-order harmonic currents from the line. However, harmonic currents of low order do occur when the line voltage is unbalanced or distorted. As might be expected from a nonlinear load, the ASVC currents include harmonics not present in the line voltage. It is important to understand ASVC behaviour under these conditions as it can influence equipment rating and component selection.

The ASVC harmonic currents can be calculated by postulating a set of harmonic voltage sources in series with the ASVC tie lines as shown in Fig. 12. If further neglect losses (i.e. \( R_f = 0, R_i = \infty \)) and assume the steady-state condition, \( \alpha = 0 \) and \( \omega = \omega_b \), eqns. 19 are modified as follows:

\[
\begin{bmatrix}
0 & \omega_b & k_n \omega_b \\
-\omega_b & 0 & 0 \\
-\frac{3}{2} k_n \omega_b & 0 & 0
\end{bmatrix}
\begin{bmatrix}
\dot{f}_d \\
\dot{f}_q \\
v_{sd}
\end{bmatrix}
= \frac{k_n \omega_b}{L} \begin{bmatrix}
\dot{f}_d \\
\dot{f}_q \\
v_{sd}
\end{bmatrix}

\begin{bmatrix}
0 \\
-\omega_b & 0 \\
-\frac{3}{2} k_n \omega_b & 0 & 0
\end{bmatrix}
\begin{bmatrix}
|v'| + v_{sd} \\
v_{sd}
\end{bmatrix}
\]

where \( v_{sd} \) and \( v_{sq} \) are the \( d \) and \( q \) components of the harmonic voltage vector. Eqns. 26 are linear and can be solved using Laplace transforms. Consider the effect of a single balanced harmonic set of order \( n \) where negative values of \( n \) denote negative sequence. The associated harmonic voltage vector has magnitude, \( v_h \) and rotates with angular velocity \( n \omega_b \). In the synchronous reference frame it rotates with angular velocity \( (n - 1) \omega_b \) as shown in Fig. 13 and

\[
\begin{align*}
v_{sd} &= v_h \cos [(n - 1) \omega_b t] \\
v_{sq} &= v_h \sin [(n - 1) \omega_b t]
\end{align*}
\]

These sinusoidal inputs on the \( d \)- and \( q \)-axes give rise to sinewave responses \( \dot{f}_d, \dot{f}_q, \) and \( \dot{v}_{sd} \) of frequency \( (n - 1) \omega_b \). Generally \( \dot{f}_d \) and \( \dot{v}_{sd} \) do not form a balanced two-phase sinewave set. They can be resolved into a positive-sequence set and a negative-sequence set using normal two-phase phasor symmetrical components. We thus find two distinct current component vectors in response to the \( n \)-order harmonic voltage vector. Within the synchronous reference frame, these rotate with frequency \( (n - 1) \omega_b \) and \( (1 - n) \omega_b \), respectively. The corresponding ASVC line currents have frequencies \( n \omega_b \) and \( (2 - n) \omega_b \), respectively. Note also that the inverter
develops an alternating voltage component of frequency \((n - 1)\omega_0\) at its DC terminals.

Eeqs. 26 and 27 have been solved to obtain algebraic expressions for the magnitudes of these harmonic currents in the particular case where \(n = -1\) (i.e. fundamental negative sequence voltage.) In this case, the ordinals of the harmonic currents are \(-1\) and \(3\), and the magnitudes are calculated from the following:

\[
|L_{-1}| = \frac{\nu_i}{L} \frac{1 - \frac{k^2C}{8L'}}{1 - \frac{k^2C}{2L'}}
\]

\[
|i_3| = \frac{\nu_i}{4L'} \frac{1 - \frac{2L'}{1 - \frac{k^2C}{2L'}}}
\]

These expressions have been evaluated using typical parameters with \(v_i = 1\) p.u., and are plotted against per-unit capacitive reactance in Fig. 14. Notice that for \(C = 2L/k^2\) both \(L_{-1}\) and \(i_3\) become infinite. This condition occurs if the second harmonic of the line frequency is equal to the ASVC-resonant-pole frequency defined in eqn. 23. Also, when \(C = 8L/k^2\), \(L_{-1}\) is zero and the ASVC draws no negative-sequence fundamental current from the line.

![Diagram of ASVC current components with fundamental negative-sequence voltage on the line](image1)

**Fig. 14** ASVC current components with fundamental negative-sequence voltage on the line

\(k = 4/n, L = 0.15\) p.u., \(R_s = 0, R_v = \infty\)

4 **Experimental results from ASVC scaled model**

It is beyond the scope of this paper to discuss the EPRI ASVC scaled model in detail. However, two sets of measured waveforms from the model are presented in Figs. 15 and 16 to illustrate the system behaviour under transient conditions. Fig. 15 shows the dynamic response of the instantaneous reactive current controller. In this case a squarewave reference, \(i_q^*\), is injected, and the oscillogram shows \(i_1, \alpha, \) and the ASVC line currents. Fig. 16 shows the ASVC response to a simulated transient unbalanced fault. In this case, the full ASVC control system is functional and \(i_q^*\) comes from the system voltage controller. Initially, the ASVC is supplying 1 p.u. capacitive VARS to the line. A phase-to-neutral fault, lasting approximately five cycles, is simulated and the oscillogram shows the associated ASVC currents. Note that the reactive current reference, \(i_q^*\) is limited in magnitude to 2 p.u.

![Oscillogram of ASVC system response to line-to-neutral fault](image2)

**Fig. 16** ASVC system response to line-to-neutral fault

5 **Conclusions**

There is every indication that ASVCs will be an important part of power transmission systems in the future. A sound analytical basis has now been established for studying their dynamic behaviour. The mathematical model derived here can readily be extended to represent
the ASVC in broader system studies. The ASVC analysis has also led to control-system designs for both Type I and Type II voltage-sourced inverters. The Type II inverter control is particularly significant because it makes it possible to obtain excellent dynamic performance from the lowest cost inverter and transformer combination.

6 References
4 Electric Power Research Institute: 'Development of an advanced static VAR compensator'. Contract RP3002-1